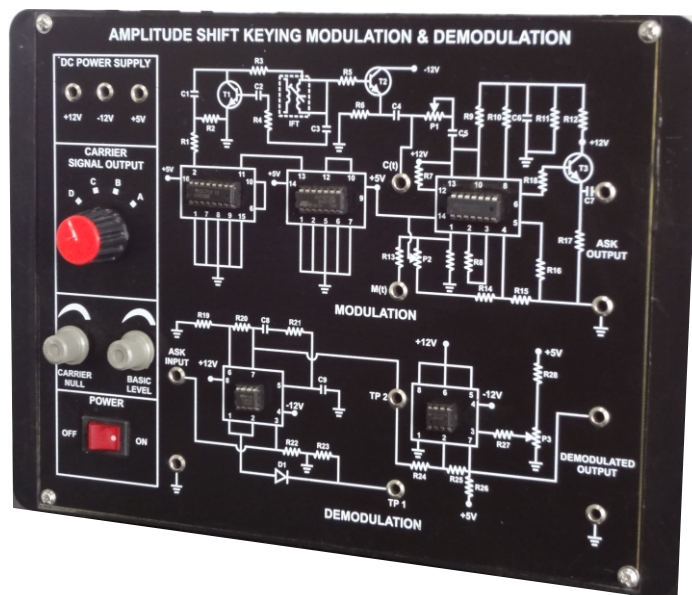


AMPLITUDE SHIFT KEYING MODULATION AND DEMODULATION



AMPLITUDE SHIFT KEYING MODULATION AND DEMODULATION

MODEL NO : AT 107

Objective :

- Observe Shift in Carrier Amplitude according to the Binary Input i.e. Digital Clock Signal.
- Recovery of Data Signal from Demodulator Circuit

Technical Specification :

- In built IC based DC Regulated Power Supply $\pm 12V$, $+5V$ / 250mA
- On board Data Signal Generator, Frequency 5KHz, Amplitude : 4Vpp Approx.
- On board Carrier Signal Generator, Frequency : 100KHz, Amplitude : 5Vpp Approx.
- Modulation using Linear Multiplier (Balanced Modulator using IC LM 1496).
- Demodulation of Diode as Envelop Detector, Low Pass Filter using IC TL 082 followed by Comparator IC using LM 311.
- Glass Epoxy PCB used as front panel & mounted on light weight shock proof plastic cabinet

Size (mm) : 280 x 205 x 85

Power Requirement : 220V AC $\pm 10\%$, 50Hz

Standard Accessories : Patch Cords & Instruction Manual

Optional Accessories : CRO 20MHz

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